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EXAMINER	
THOMPSON, JAMES A	

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Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

DETAILED ACTION

Response to Arguments

Applicant's arguments filed 21 January 2009 have been fully considered but they are not persuasive.

Regarding page 1 to page 3, line 6: In the final office action of 18 November 2008, Examiner did not state that the PC Card Controller [88] is the terminal device. Rather, as explained in the corresponding parenthetical, the PC card controller [88] is connected to and communicates with CPU [85] of the data processing device via an interface [83], as shown in figure 2 of Murata. Thus, it is the data processing device connected via the interface that Examiner was stating to be the terminal device, not the PC Card Controller. In fact, Examiner later states that the connected computer terminal can read and process print data from the storing unit [column 8, lines 55-60 of Murata] and write image and command data to the storing unit [column 9, lines 5-12 of Murata]. It is the data processing device, with its corresponding CPU, that corresponds to the terminal device which performs associated functions of the terminal device recited in independent claims 1, 13 and 14.

Additionally, the data processing device writes image and command data to the storing unit [column 9, lines 5-12 of Murata] which, when detected, results in the automatic execution of the print job and/or scan job stored in the storing device [column 6, lines 41-45 and lines 54-58 of Murata]. Therefore, the function implementing unit, which is the corresponding portion of embodied software executed by the CPU, executes a process to implement one of the one or more [printing and/or scanning] functions that is indicated by instruction data when the instruction data is stored in the storing unit by the terminal device, as required by claims 1, 13 and 14.

Regarding page 3, line 7 to page 4, line 11: Again, Examiner stated that the data processing device, and not the PC Card Controller, is the terminal device. Figure 3 of Murata shows the contents of

the stored file, which is downloaded by the user [column 7, lines 2-7 of Murata]. A subsequent section of Murata describes textually what is shown in figure 3 of Murata. Column 7, line 8 to column 8, line 7 of Murata describe the various stored function which have different settings that a user can select. The type of variables shown in figure 3 of Murata are user-defined variables, such as sorting, stapling and punching, and column 7, line 60 to column 8, line 7 of Murata makes explicit what should be understandable from figure 3 of Murata, namely that the user creates the command file.

Regarding page 4, line 12 to page 5, line 6: While an interface does perform some different functions than a CPU bus, the combination of the PC Card Controller [88] and the CPU bus [83] acts as an interface since, as stated in the final office action of 18 November 2008, "PC card controller (88) is connected to and communicates with CPU (85) of data processing device via an interface (83) [as shown in figure 2 of Murata]. The CPU bus does accept and direct inputs and outputs. While that may or may not be sufficient to establish it as an interface, when combined with the PC Card Controller, Murata does teach the recited interface.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JAMES A. THOMPSON whose telephone number is (571)272-7441. The examiner can normally be reached on 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward L. Coles can be reached on 571-272-7402. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/James A Thompson/
Primary Examiner, Art Unit 2625

05 February 2009